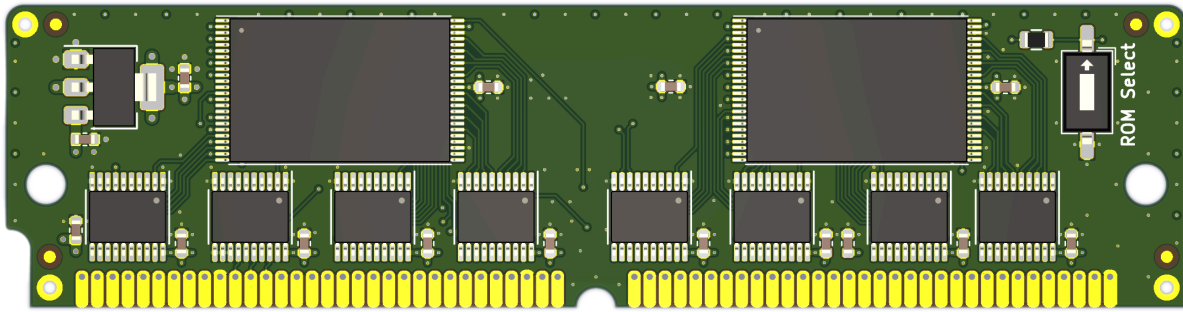


Garrett's Workshop

GW4402A

8MB ROM SIMM for Macintosh SE/30 and Macintosh II-series

User's Guide



Overview

The GW4402A 64-pin ROM SIMM replaces the ROM SIMM of a Macintosh II-series or Macintosh SE/30 computer. GW4402A comes preprogrammed with a customized, 32-bit clean Macintosh ROM. The customized ROM allows your Macintosh to use more than 8MB of RAM and also features a 7.5MB bootable ROM disk preloaded with System 7 and several utilities.

Compatibility

GW4402A is compatible with the following Macintosh computers:

- Macintosh IIx
- Macintosh IIcx
- Macintosh IIci
- Macintosh IIfx
- Macintosh IIsi
- Macintosh SE/30

Fast Startup

It takes about 10 seconds to boot from the included ROM-based System 7.1, so you can begin working with programs or playing games almost instantly.

ROM Disk Settings

A control panel allows your preferences for the built-in ROM disk to be changed. You can boot from the ROM disk, mount it under another boot disk, or hide it from the desktop completely. Of course, you can always press "R" during startup to boot from ROM independent of your saved preferences.

It's also possible to enable RAM disk mode, which allows writing data to the ROM disk, although data stored is not retained between restarts. The RAM disk is enabled by pressing "A" during startup, or by enabling the corresponding setting in the control panel.

Dual ROM Images

In addition to the customized ROM with the System 7 ROM disk, GW4402A also includes an unmodified Macintosh IIsi ROM image. The desired ROM image can be selected using the DIP switch in the upper right corner of the board. Both ROM images can be rewritten using a ROM SIMM Programmer such as the one sold by Big Mess o' Wires.

Ecologically Friendly, Gold-Plated PCB

GW4402A features a lead-free, ENIG gold-plated, 4-layer PCB and is fully EU RoHS-compliant. All units are tested extensively before shipment. Only new parts are used to build GW4402A, and all GW4402A ROM SIMM modules are manufactured in our semi-automated facility in Columbus, Ohio, in the United States.

Open-Source Design

GW4402A's design is fully open-source. The schematics, board layouts, and driver software are all freely available for commercial and noncommercial use. To download the design files, visit the Garrett's Workshop GitHub page, <https://github.com/garrettsworkshop>.

Installation

The GW4402A ROM SIMM is installed into the ROM SIMM slot on your Macintosh's motherboard. On Macintosh II-series machines, the ROM SIMM socket is accessible by just removing the computer's top cover. On Macintosh SE/30 machines, the computer must be disassembled and the motherboard removed in order to install the SIMM.

Installation Procedure for Macintosh SE/30

To install the GW4402A ROM SIMM in a Mac SE/30 machine, disassemble the computer, remove the existing ROM SIMM, replace it with the new SIMM, then reassemble the computer.

Installation Procedure for Macintosh II-series

To install the GW4402A ROM SIMM in a Macintosh II-series machine, open the machine's top cover and locate the ROM SIMM socket as well as the jumper labeled "W1."

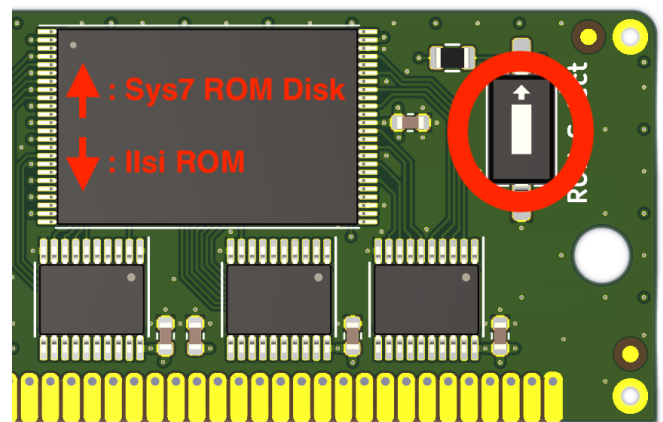
If your Macintosh II-series machine has a ROM SIMM already installed, then you need only to substitute the existing SIMM with the new SIMM.

However, if your Macintosh II-series machine boots without a ROM SIMM installed, then your machine has ROM chips soldered directly to the motherboard. These must be disabled in order to utilize a ROM SIMM. To disable the motherboard ROM, you must change the "W1" jumper setting to the opposite of its default state. That is, if your machine has the "W1" jumper installed, then it must be removed in order to use the SIMM in the ROM SIMM socket. On the other hand, if your machine boots without a ROM SIMM but does not have the "W1" jumper installed by default, install the provided jumper when a ROM SIMM is installed.

If you become confused as to which jumper setting is correct, remove any ROM SIMMs and also remove the W1 jumper. With both removed, attempt to boot the machine. If the Mac "chimes" and begins to boot, then W1 must be installed when a ROM SIMM is installed. Otherwise, if the Mac is silent and does not boot, W1 must remain removed when a ROM SIMM is installed.

ROM Select DIP Switch

The "ROM Select" DIP switch allows you to select between the customized ROM with ROM disk and an unmodified Macintosh IIsi ROM. To enable the ROM disk, set the switch to the upper position. To use the unmodified Macintosh IIsi ROM, set the switch to the lower position.



Booting from the ROM Disk

To boot from the ROM disk, ensure the “ROM Select” DIP switch is set to the upper position and press “R” repeatedly. If you require a writable boot disk, for example to use AppleTalk, you can repeatedly press “A” instead and the system will boot from ROM but copy the contents of the ROM disk into RAM. The RAM disk function requires at least 16 MB of memory.

Mounting the ROM Disk

By default, when booting from a volume other than the ROM disk, the ROM disk volume is mounted on the desktop as would be any other drive. This functionality can be disabled using the GW ROM Disk control panel.

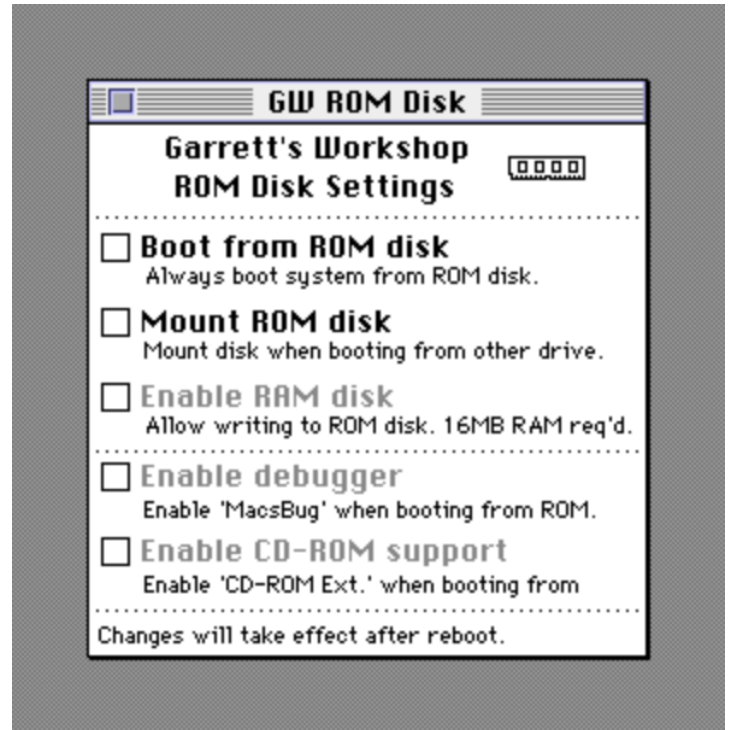
Changing Default Settings Using the GW ROM Disk Control Panel

The GW ROM Disk control panel allows various ROM disk settings to be changed. Settings are stored in PRAM and can be reset by “zapping” the PRAM at boot using the Cmd+Opt+P+R key combination.

The control panel allows selection of the default boot settings, including whether the machine should always boot from ROM rather than an internal SCSI disk. It’s also possible to enable or disable mounting of the ROM disk volume under another boot disk. The control panel also allows MacsBug and the Apple CD-ROM extension to be enabled and disabled.

Pressing “R” or “A” to boot from ROM disk always overrides the defaults set in the control panel and causes MacsBug and Apple CD-ROM to be disabled.

MacsBug requires 32 MB RAM when enabled in conjunction with the RAM disk. CD-ROM support slows booting substantially when a CD-ROM drive is not present and should be disabled unless a CD-ROM drive is installed.



Updating the ROM Image (for advanced users)

The GW4402A ROM SIMM can be flashed using a SIMM programmer. Currently the only supported SIMM programmers are the Big Mess o' Wires ROM SIMM Programmer and the dougg3 ROM SIMM Programmer.

Programmer Firmware Update

In order to program GW4402A using one of these programmers, the programmer firmware must be updated with a special firmware supporting the Garrett's Workshop ROM SIMM. The same firmware is compatible with the BMoW as well as dougg3 programmers and can be applied using the ROM SIMM Programmer utility's "update firmware..." function located in the "advanced" menu. Ensure that your computer remains on and connected to the programmer board during the firmware update, and only apply valid update files to the programmer board, otherwise the programmer may be rendered inoperable. Be aware that with the Garrett's Workshop firmware, the programmer is only able to flash the GW4402-series ROM SIMM. To program another ROM SIMM, the original firmware must be restored to the programmer board. All of the required files are available on our website, including the programmer firmware update and well as the ROM SIMM ROM images, are available on our website, <http://garrettsworkshop.com>.

Programming a ROM to Each Switch Position

Two ROM images can be flashed to the GW4402A ROM SIMM, with some caveats. One of the ROM images flashed to the SIMM can be as large as 8 MB in size, while the other ROM image must be 2 MB or smaller. With that exception, any ROM can be flashed to either of the "ROM select" switch positions. The larger ROM image must be flashed to the SIMM first using the "erase/write entire SIMM" option and with the switch set to the desired position for the image. The smaller ROM can then be flashed using the "only erase first ..." option with the switch set to the opposite position.

Technical Specifications

Physical Dimensions

Parameter	Value
Height	25.019 mm \pm 0.2 mm
Width	97.79 mm \pm 0.2 mm
Thickness	< 10 mm
Weight	< 28 g

Electrical Specifications

Specifications are valid over temperature range of 0 °C – 85 °C and $V_{CC} = 4.5\text{ V} - 5.5\text{ V}$.

Parameter	Value	Conditions
V_{IHmin}	2.0 V	
V_{ILmax}	0.8 V	
V_{OHmin}	2.5 V	$I_{OH} = -4\text{ mA}$
V_{OLmax}	0.5 V	$I_{OL} = 4\text{ mA}$
Output Slew Rate	< 1.5 V/ns	
I_{Imax}	$\pm 20\ \mu\text{A}$	$V_{in} = 0\text{ V} - 5.5\text{ V}$
C_{IOmax}	20 pF	data bus D[31:0] address bus A[22:0], nCS, nOE, nWE
I_{CCmax}	80 mA	$V_{CC} = 5\text{ V}$